

SPECIFICATION FOR SMT VCXO MtronPTI P/N 1674-005

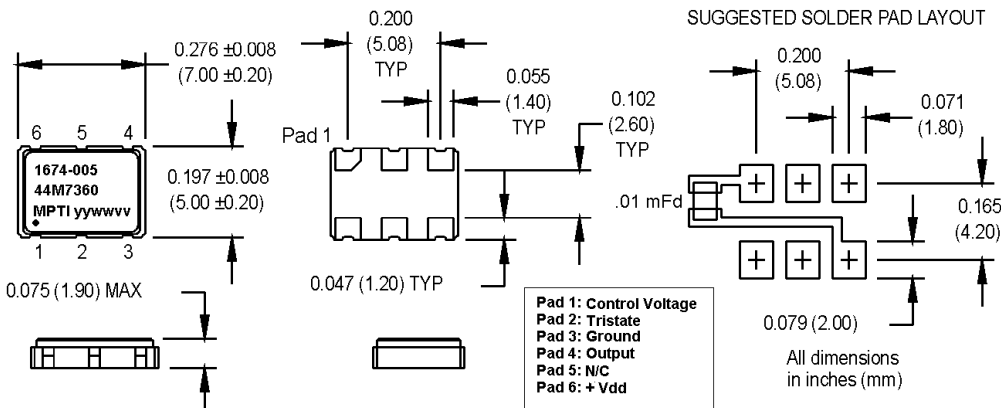
I. GENERAL & ELECTRICAL REQUIREMENTS:

1. FREQUENCY OF OPERATION: 44.736000 MHz
2. FREQUENCY STABILITY VS. TEMPERATURE: ± 50 ppm (Inclusive of initial tolerance, operating temperature, voltage & load variation, shock, vibration, and aging.
3. OPERATING TEMPERATURE RANGE: -40°C to $+85^{\circ}\text{C}$
4. OPERATING VOLTAGE: 3.3 V $\pm 5\%$
5. OPERATING CURRENT: 25 mA max.
6. OUTPUT LOGIC: CMOS/TTL Compatible
7. SYMMETRY: 45/55% ref. to $\frac{1}{2} V_{cc}$
8. RISE/FALL TIME: 5 nS max. ref. to 20% to 80%
9. OUTPUT LOGIC LEVELS: $V_{OL} = 0.33$ v max. $V_{OH} = 2.97$ min.
10. OUTPUT LOAD: 15 pF max.
11. PULLABILITY (APR): ± 50 ppm
12. CONTROL VOLTAGE (Pin 1): 0.3 V to 3.3 V (Center frequency at + 1.65 V)
13. LINEARITY: 10% max. with positive monotonic slope.
14. JITTER: 10 pS RMS max.
15. TRISTATE FUNCTION (Pad 2): Logic level "1" or floating, enables output.
Logic level "0" output disabled to a high impedance.

II. ENVIRONMENTAL & MECHANICAL REQUIREMENTS:

1. SHOCK: MIL-STD-202, Method 213, Condition C.
2. VIBRATION: MIL-STD-202, Methods 201 & 204.
3. HERMETICITY: 1×10^{-8} atm cc/sec min.
4. MAXIMUM SOLDERING CONDITIONS: See Figure 1.
5. SOLDERABILITY: Per EIAJ-STD-002
6. PACKAGE: 6- Pad leadless ceramic. RoHS compliant.

III. DIMENSIONS:



IV. DATA SHEET REVISION TABLE:

Date	Rev.	PCN	Details of Revision
10/16/01	0	N/A	Original release.
2/13/02	A	N/A	Changed Jitter Spec to 10 ps RMS max. Supersedes 10/16/01 release.
4/23/07	B	N/A	Added RoHS compliance information. Part has always been RoHS compliant. Supersedes 2/13/02 release.

Figure 1

