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## SPECIFICATION FOR SMT OSCILLATOR MtronPTI P/N M2002S534

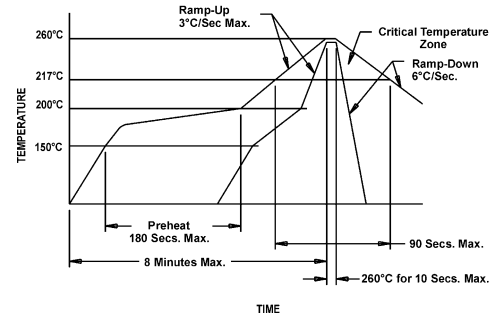
### I. GENERAL & ELECTRICAL REQUIREMENTS:

1. FREQUENCY OF OPERATION: 75.033000 MHz
2. FREQUENCY STABILITY:  $\pm 50$  ppm max.
3. FREQUENCY VS. AGING:  $\pm 3$  ppm first year typical.  $\pm 2$  ppm/year typical thereafter.
4. OPERATING TEMPERATURE RANGE:  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$
5. OPERATING VOLTAGE:  $3.3\text{ V} \pm 0.3\text{V}$
6. OPERATING CURRENT: 55 mA max.
7. OUTPUT TYPE: HCMOS/TTL Compatible
8. SYMMETRY: 45/55% ref. to  $\frac{1}{2} V_{\text{dd}}$
9. RISE/FALL TIME: 4.0 nS max. ref. 10% to 90% Vdd
10. OUTPUT LOGIC LEVELS:  $V_{\text{OL}} = 10\% V_{\text{dd}}$  max.  $V_{\text{OH}} = 90\% V_{\text{dd}}$  min.
11. OUTPUT LOAD: 15 pF/2 TTL max.
12. RANDOM JITTER: 12 pS RMS max.
13. TRISTATE FUNCTION (Pad 1): Logic "high" or "floating", clock signal output  
 Logic "low", output disables to high impedance state.
14. START-UP TIME: 10 mS max.

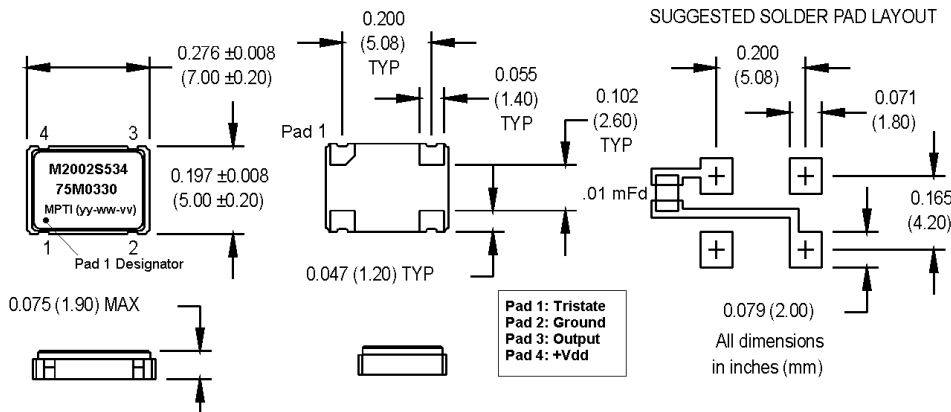
### II. ENVIRONMENTAL & MECHANICAL REQUIREMENTS:

1. SHOCK: MIL-STD-202, Method 213, Condition C.
2. VIBRATION: MIL-STD-202, Methods 201 & 204.
3. HERMETICITY:  $1 \times 10^{-8}$  atm cc/sec min.
4. STORAGE TEMPERATURE:  $-55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$
5. SOLDERABILITY: Per EIAJ-STD-002
6. REFLOW SOLDER CONDITIONS: See Figure 1
7. PACKAGE: 4 - Pad leadless ceramic package.  
 Tin-lead (SnPb) solder dipped pads.

Figure 1



### III. DIMENSIONS:



### IV. DATA SHEET REVISION TABLE:

Date	Rev.	PCN	Details of Revision
1/13/06	0	N/A	Original release.
3/21/08	A	N/A	Added Raytheon P/N reference. Supersedes 1/13/06 release.