

SPECIFICATION FOR RoHS 6 COMPLIANT HCMOS SMT OSCILLATOR MtronPTI P/N M2002T034

Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Frequency of Operation	F _o		25.000000		MHz	
Frequency Stability						
vs. Temperature	ΔF/F	-25		+25	ppm	
RF Output						
Output Type		HCMOS Compatible				
Output Load				15	pF	
Symmetry (duty cycle)	T _{DC}	45	50	55	%	Ref to ½ V _{DD}
Logic "1" Level	V _{OH}	90% V _{DD}			V	HCMOS load
Logic "0" Level	V _{OL}			10% V _{DD}	V	HCMOS load
Rise/Fall Time	T _R /T _F			6.0	nS	From 20% to 80% V _{DD}
Output Current	I _{OL}	+2			mA	HCMOS load
	I _{OH}	-2			mA	HCMOS load
Tristate Logic		2.2 or N/C			V	Pad 1: Output Enabled
				0.8	V	Pad 1: Output Disabled to high-Z
Start-Up Time				10	mS	
Supply Voltage & Power Consumption						
Operating Voltage	V _{DD}	2.97	3.3	3.63	V	
Operating Current	I _{DD}			10	mA	
Additional Specifications						
Phase Jitter (RMS)	Φ _J			1.0	pS	12KHz to 20MHz

Environmental & Mechanical Requirements:

Operating Temperature	T _A	0		+70	°C	
Storage Temperature	T _S	-55		+125	°C	
Mechanical Shock	Per JESD22-B-104-A 1500g Peak 0.5 msec duration 5 pulses in 6 directions					
Vibration	Per JESD22-B-103-A 20g Peak (20-2000 Hz) in X, Y and Z directions					
Thermal Cycle	Per MIL-STD-883, Method 1010, B (-55°C to 125°C, 15 min. dwell, 10 cycles)					
Hermeticity	Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm cc/s of Helium)					
ESD-Human Body Model	2000 volts; Per JESD22A-114-A					
ESD-Machine Model	200 volts; Per JESD22A-115-A					
Solderability	Per EIAJ-STD-002					
Max. Soldering Conditions	See solder profile, Figure 1					
Package Type	5.0 x 7.0 x 1.7 mm, 4-pad Ceramic Leadless Chip Carrier (M2 type)					
Termination (Pad) Material	10 to 15 μm of tungsten (W), 1.29 to 8.89 μm of nickel (Ni), 0.3 to 1.0 μm of gold (Au)					
Moisture Sensitivity Level	1					

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Dimensions, Marking, and Pin Out Information:

Pad	Function
1	Tristate
2	Ground
3	Output
4	+V _{DD}

Part Marking	
Line 1	M2002T034
Line 2	25M0000
Line 3	M yy ww vv

Legend	
yy	Year
ww	Work week
vv	Factory code

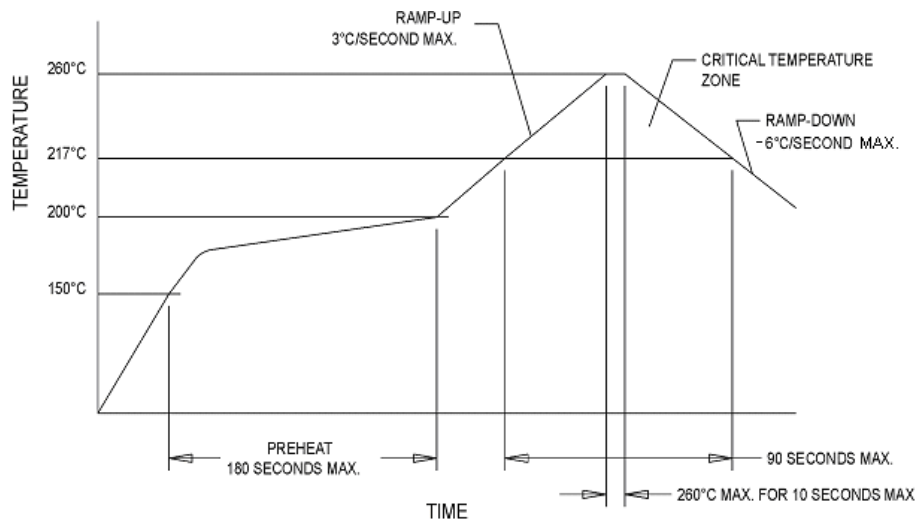
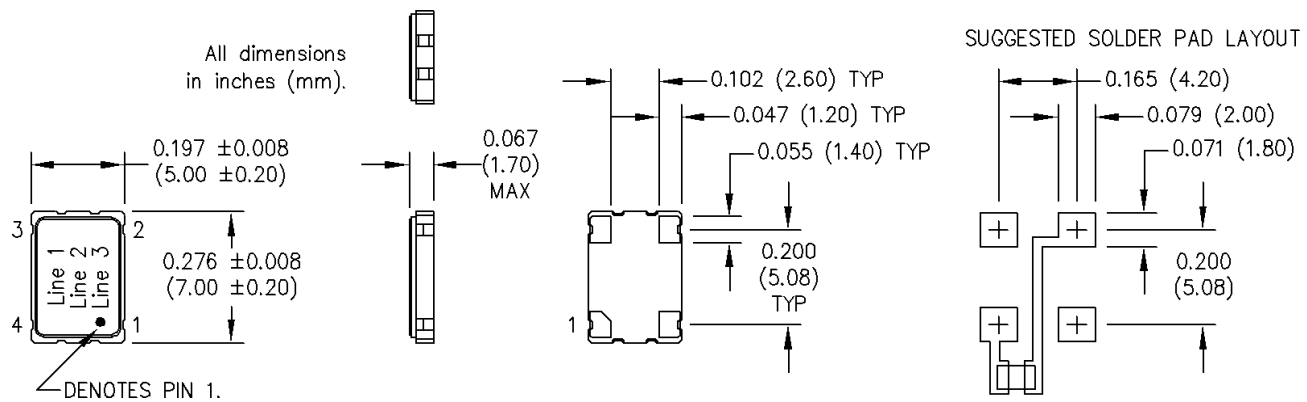


Figure 1

Datasheet Revision Table:

Date	Rev.	Author	Details of Revision
11/19/10	0	DDM	Original release.
11/27/13	A	MM	Added Phase Jitter specification and changed to new datasheet format.