

SPECIFICATION FOR HCMOS COMPATIBLE SMT OSCILLATOR MtronPTI P/N M2061S001

Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Frequency of Operation	F _o		12.288000		kHz	
Frequency Stability						
Frequency Stability	ΔF/F	-50		+50	ppm	Initial frequency @ +25°C and deviation over the operating temperature range.
Output						
Output Type		HCMOS/TTL Compatible				
Output Load				15/2	pF/TTL	
Symmetry (duty cycle)	T _{DC}	45	50	55	%	Ref to ½ V _{DD}
Logic "1" Level	V _{OH}	90% V _{DD}			V	HCMOS load
Logic "0" Level	V _{OL}			10% V _{DD}	V	HCMOS load
Rise/Fall Time	T _R /T _F			10	nS	From 10% to 90% V _{DD}
Start-Up Time				10	mS	
Tristate Function		Logic (70% V _{DD} min) or floating Logic (30% V _{DD} max) to a high-Z state				Pad 1: Enables output Pad 1: Disables output
Supply Voltage & Power Consumption						
Operating Voltage	V _{DD}	3.135	3.3	3.465	V	
Operating Current	I _{DD}			5	mA	

Environmental & Mechanical Requirements:

Operating Temperature	T _A	-40		+85	°C	
Storage Temperature	T _S	-55		+125	°C	
Mechanical Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sinewave)					
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
Thermal Cycle	Per MIL-STD-883, Method 1010, B (-55°C to 125°C, 15 min. dwell, 10 cycles)					
Hermeticity	Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm cc/s of Helium)					
Solderability	Per EIAJ-STD-002					
Max. Soldering Conditions	See solder profile, Figure 1					
Package Type	4-pad 2.0 X 1.6 X 0.8 mm leadless ceramic. RoHS compliant.					

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Dimensions, Marking, and Pin Out Information:

Pad	Function
1	Tristate Control
2	Circuit / Case Ground
3	Clock Signal Output
4	Supply Voltage (V_{DD})

Part Marking	
Line 1	12M28
Line 2	M (y-ww)

Legend	
y	Year
ww	Work week

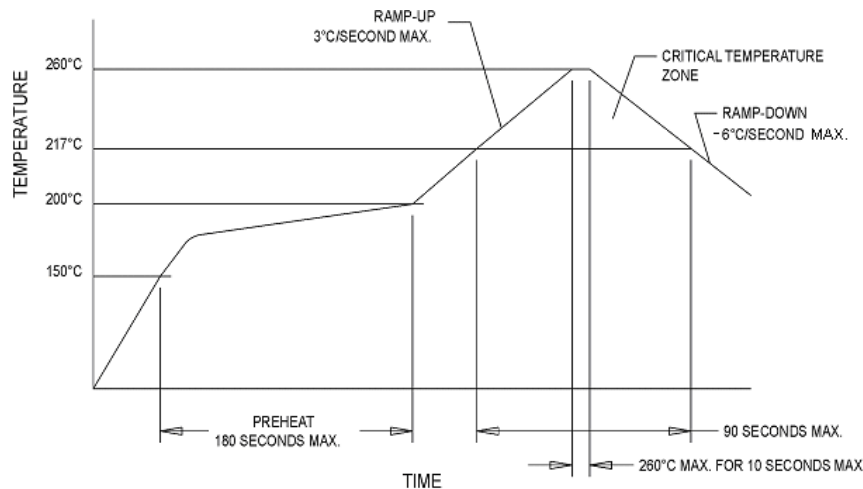
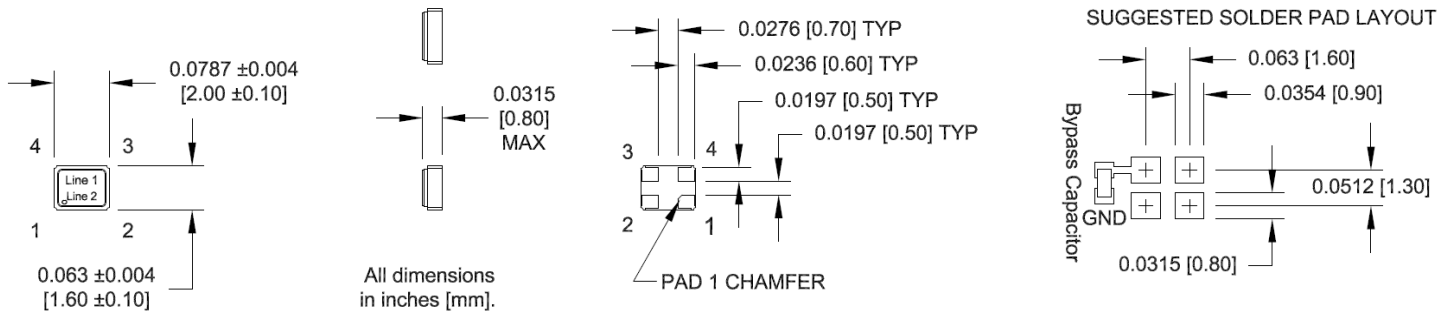


Figure 1

DATA SHEET REVISION TABLE:

Date	Rev.	Author	Details of Revision
12/11/13	0	MM	Original release.