



SPECIFICATION FOR RoHS 6 COMPLIANT SMT VCTCXO MtronPTI P/N M6065S020

Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Frequency of Operation	F _o		16.000000		MHz	
Frequency Stability						
Initial Tolerance	ΔF/F	-1.5		+1.5	ppm	@ +25°C, with V _c = 1.5V
Frequency Stability	ΔF/F			+0.5	ppm	(F _{max} – F _{min})/2, over operating temperature range
Frequency vs. Supply		-0.2		+0.2	ppm	For a ±5% supply voltage change
Frequency vs. Load		-0.2		+0.2	ppm	For a ±10% load change
Aging	F _A	-1.0		+1.0	ppm	Per year
Output						
Output Type		Clipped Sine Wave				External DC-cut output capacitor (0.001 μF) required.
Output Load		10 kΩ 10 pF				± 10%
Output Levels	V _{OUT}	0.8			V _{pk-pk}	Load = 10K 10 pF
Startup Time	t _{SU}			10	ms	
Control Voltage	V _c	0.5	1.5	2.5	V	
Tuning Range		±5.0			ppm	
Additional Specifications						
SSB Phase Noise (Under Static Conditions)			-85		dBc/Hz	@ 10 Hz Offset
			-110		dBc/Hz	@ 100 Hz Offset
			-135		dBc/Hz	@ 1 kHz Offset
			-150		dBc/Hz	@ 10 kHz Offset
Operating Voltage	V _s	2.85	3.00	3.15	V _{DC}	
Operating Current	I _s			2.0	mA	

Environmental & Mechanical Requirements:

Operating Temperature	T _A	-40		+85	°C	
Storage Temperature	T _S	-55		+105	°C	
Shock	Per MIL-STD-202, Method 213, Condition C					
Vibration	Per MIL-STD-202, Methods 201 & 204					
Maximum Soldering Conditions	See Figure 1.					
Solderability	See solder profile, Figure 1					
Package Type	2.5 x 2.0 mm, 4-pad Ceramic Leadless Chip Carrier. RoHS Compliant					

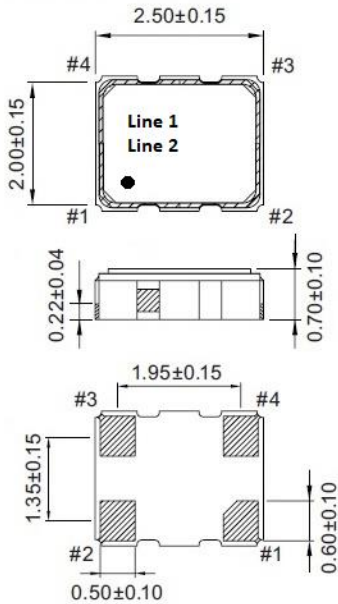
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Dimensions, Pin Out and PAD Layout Information:

Part Marking	
Line 1	16M000
Line 2	M y m vv

Legend	
y	Last digit of year
m	Month letter code
vv	Factory code

Pad	Function
1	Tuning Voltage
2	Ground
3	Output
4	+V _{DD}



SUGGESTED SOLDER PAD LAYOUT

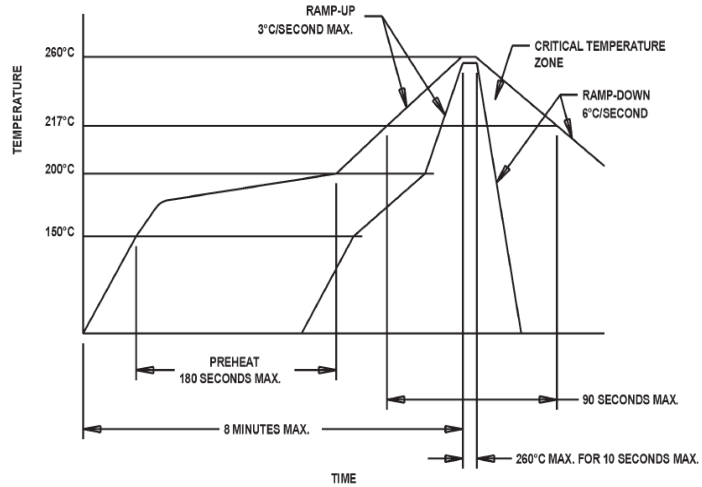
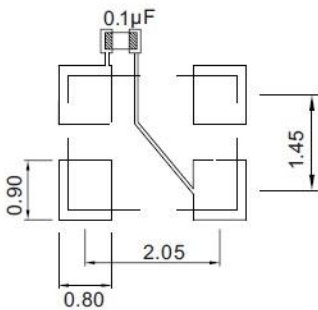


Figure 1

Data Sheet Revision Table:

Date	Rev.	Author	Details of Revision
05/10/19	0	MM	Original Release.